



(University of Choice)

MASINDE MULIRO UNIVERSITY OF SCIENCE AND TECHNOLOGY (MMUST)

MAIN CAMPUS

SPECIAL/SUPPLEMENTARY UNIVERSITY **EXAMINATIONS** 2021/2022 ACADEMIC YEAR

THIRD YEAR FIRST SEMESTER EXAMINATIONS

FOR THE DEGREE OF BACHELOR OF SCIENCE IN ELECTRICAL AND COMMUNICATIONS ENGINEERING

COURSE CODE:

ECE 316

COURSE TITLE: DIGITAL ELECTRONICS I

DATE: 28TH JULY 2022

TIME: 11:00 - 1:00 PM

INSTRUCTIONS TO CANDIDATES

ANSWER QUESTION ONE AND ANY OTHER TWO QUESTIONS. QUESTION ONE CARRIES 30 MARKS AND ALL OTHERS 20 MARKS EACH.

MMUST observes ZERO tolerance to examination cheating This Paper Consists of 2 Printed Pages. Please Turn Over.

30 MARKS OUESTION ONE a) State the main advantages of digital over analog systems. (4 marks) (4 marks) b) Perform the following conversions: the decimal number 430 to Excess-3 code i. the binary number 10110 to Gray code ii. c) Perform following subtraction: (6 marks) 11001-10110 using 1's complement i. ii. $(7)_{10} - (11)_{10}$ using 2's complement (8 marks) d) Prove the following equations using the Boolean algebraic theorems: $A + \bar{A}.B + A.\bar{B} = A + B$ i. $\bar{A}BC + A\bar{B}C + AB\bar{C} + ABC = AB + BC + AC$ e) Verify that the following operations are commutative but not associative: (8 marks) i. **NAND** ii. **NOR QUESTION TWO** 20 MARKS a) Distinguish between min terms and max terms. marks) b) Minimize the following logic function using K-maps and realize using NAND and NOR gates. (8 marks) $F(A,B,C,D) = \sum m(1,3,5,8,9,11,15) + d(2,13)$ c) A staircase light is controlled by two switches one at the top of the stairs and another at the bottom of stairs. i) Make a truth table for this system. ii) Write the logic equation is SOP form. (10 marks) iii) Realize the circuit using AND-OR gates. 20 MARKS **OUESTION THREE** a) Reduce the following equation $Y = B\overline{C}\overline{D} + \overline{A}B\overline{C}D + AB\overline{C}D + \overline{A}BCD + \overline{A}BCD$ using a k-map. (8 marks) b) With the help of a suitable diagram, explain how you would implement a JK flip-flop to T type (12 marks) flip-flop. 20 MARKS **QUESTION FOUR** a) What is a flip-flop? What is the difference between a latch and a flip-flop? List out the (6 marks) applications of flip-flops. b) With the help of clocked JK flip flops and waveforms, explain the working of a three bit binary

ripple counter. Write truth table for clock transitions.

(14 marks)