05



(University of Choice)

# MASINDE MULIRO UNIVERSITY OF SCIENCE AND TECHNOLOGY (MMUST)

#### MAIN CAMPUS

### UNIVERSITY EXAMINATIONS 2021/2022 ACADEMIC YEAR

### THIRD YEAR SPECIAL/SUPPLEMENTARY EXAMINATIONS

## FOR THE AWARD OF DIPLOMA IN ENGINEERING

COURSE CODE: DE

**DEE 083** 

COURSE TITLE:

**MICROPROCESSORS** 

**DATE:** Wednesday 5<sup>th</sup> Oct, 2022

**TIME:** 4.00 p.m - 6.00 P.m

### **INSTRUCTIONS TO CANDIDATES**

ANSWER QUESTION ONE AND ANY OTHER TWO QUESTIONS.
QUESTION ONE CARRIES 30 MARKS AND ALL OTHERS 20 MARKS EACH.

TIME: 2 Hours

MMUST observes ZERO tolerance to examination cheating

This Paper Consists of 3 Printed Pages. Please Turn Over.

### **Question One**

| a.             | Distinguish between the following terms; <i>kilobyte, megabyte, gigabyte, teword</i> and <i>petabyte</i>   | erabyte, long<br>[3 marks]                              |
|----------------|--|---|
| b.             | Indicating the corresponding voltage levels explain the logic convention hence explain how RS 232C is interfaced to TTL.   | in RS 232C,<br>[5 marks]                                |
| c.             | Explain different types of registers in 8085 microprocessor architecture.  | [5 marks]   |
| d.             | Using relevant diagrams explain clearly the general purposes of data regimarks]  | sters.  |
|                |  |   |
| e.             | Explain the instructions (i) LDS (ii) PUSHF (iii) TEST (iv) CLD  | [6 marks]   |
| f.             | Write down the steps, sequentially carried out by 8086 when an interrupt   | occurs.   |
|                | marks]   |   |
| Quest<br>a.    | Explain the meaning of the phrase addressing mode  i. Name the three addressing modes of the 8086  ii. Outline the six memory addressing modes of the 8086  iii. Using assembly language, give examples each of immediate address register addressing.  marks] | [0.5marks]<br>[1.5marks]<br>[3 marks]<br>sing and<br>[4 |
| b.             | List all the flags in 8085 microprocessor.   | [2 marks]   |
| C.             | Give the two important reasons why although the 8086 is a 16-bit process with 8-bit memory. marks]   | or it deals [2  |
| d.             | Enumerate any five advantages of memory segmentation.  | [6 marks]   |
| Quest          | ion Three  |   |
| a.             | Explain the concept of instruction queue in the 8086.  | [2 marks]   |
| b.             | Explain the terms as applied in microprocessors  i. Simplex transmission  ii. Duplex transmission  iii. Half-duplex transmission   | [1 mark]<br>[1 mark]<br>[1 mark]                        |
| c.<br>d.<br>e. | What is stack? Explain the use and operation of stack and stack pointer?  Differentiate between microprocessor and microcontroller.  Compare isolated I/O and memory mapped I/O  | [5 marks]<br>[6 marks]<br>[4 marks]                     |

### **Question Four**

marks]

a. Differentiate between;i. a compiler and an interpreterii. a compiler/interpreter and an assembler.

c. List any four tasks that a microcomputer is capable of doing.

Using a cross-sectional drawing briefly explain the construction of a USB cable.
 marks]

[2 marks]

[3 marks]

[4 marks]

d. With the aid of a well labeled diagram, mention the total number of registers of 8086 and show how they are grouped.