

(University of Choice)

# MASINDE MULIRO UNIVERSITY OF **SCIENCE AND TECHNOLOGY** (MMUST)

MAIN CAMPUS

# **UNIVERSITY EXAMINATIONS 2022/2023 ACADEMIC YEAR**

## FOURTH YEAR FIRST SEMESTER EXAMINATIONS

# FOR THE DEGREE OF BACHELOR OF SCIENCE IN MECHANICAL AND INDUSTRIAL ENGINEERING

COURSE CODE:

**ECE 413** 

COURSE TITLE:

INSTRUMENTATION

DATE: 20TH DECEMBER, 2022 TIME: 12:00 PM - 2:00 PM

#### INSTRUCTIONS TO CANDIDATES

ANSWER OUESTION ONE AND ANY OTHER TWO QUESTIONS. OUESTION ONE CARRIES 30 MARKS AND ALL OTHERS 20 MARKS EACH.

TIME: 2 Hours

MMUST observes ZERO tolerance to examination cheating

This Paper Consists of 4 Printed Pages. Please Turn Over.

### Question One (30mks)

- (a) i) State and explain the working principle of the following sensors: [5mks]
  - i) Load cell
  - ii) Polyvinylidene Fluoride tactile sensor
  - iii) Thermocouple
  - iv) Hall sensor
  - v) Pyroelectric infrared sensor
  - ii) Highlight five advantages of intelligent instruments over dumb [5mks] instruments.
- (b) Describe with the aid of a circuit diagram and waveforms how an analogue output voltage from a sensor can be sampled and held before conversion to digital signal.
- (c) A linear potentiometric displacement sensor in Fig.1 has a [5mks] maximum resistance of  $R_p$  and a range of 0 to 20mm. If the sensor is supplied with a voltage  $V_s$  and connected to a digital voltmeter of input resistance  $R_L$  at terminals A and B. Determine the ratio of the output to input voltage as a function of the fractional potentiometric resistance x.

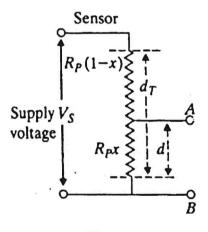


Fig.1

- (d) Outline four tasks that can be performed by LabVIEW graphical [4mks] programming environment.
- (e) Describe the working of a digital Ramp ADC using a well labelled [6mks] diagram.

### Question Two (20mks)

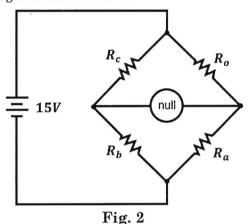
- (a) A potentiometer has a total resistance of  $12k\Omega$  over the entire [6mks] length of 24cm. The output voltage is measured by a digital voltmeter with  $R = 12M\Omega$  connected across the output terminals. If it is supplied by 12V DC and the wiper is at 18cm, calculate:
  - i) the output voltage
  - ii) the non-linearity error introduced by the digital voltmeter
- (b) If the analogue voltage from potentiometer is to fed into a 4-bit [9mks] successive approximation ADC, through some intermediate circuitry.
  - i) describe how a digital equivalent signal is generated from the value obtained in (a) (i) above.
  - ii) Draw the circuit used for conversion
- (c) Strain in a cantilever beam is monitored using a strain gauge of resistance  $1k\Omega$ , GF=2 and temperature Coefficient of resistance of  $10^{-5}$ /°C at room temperature, connected into bridge circuit. At maximum beam deformation, the strain is 4.5.

[5mks]

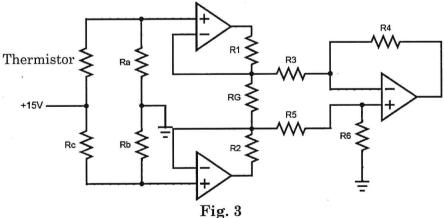
- i) Calculate the change in resistance of the gauge if it is strained 0.1%
- ii) Calculate the change in effective strain when the room temperature increases by 10°C;
- iii) Suggest a way of reducing this temperature effect.

#### Question Three (20mks)

- (a) i) Explain the working principle of negative temperature coefficient [5mks] (NTC) thermistor.
  - *ii)* An NTC thermistor made of chromium has a resistance of  $1k\Omega$  at T = 100°C and  $100k\Omega$  at 0°C. Calculate the resistance at T = 45°C.
- (b) If the Chromium thermistor (R<sub>0</sub>) is connected into a bridge circuit and [4mks] supplied with 15Vas shown in Fig. 2. Calculate the voltage that the thermistor must resolve to define 1°C change in temperature.
   Consider a balanced condition R<sub>0</sub> = R<sub>a</sub> = R<sub>b</sub> = R<sub>c</sub> = 100kΩ and temperature changes from 0 to 45°C.



(c) The differential output voltage from the bridge circuit contains high frequency signals and therefore it's fed into the input of the instrumentation amplifier circuit as shown in Fig. 3. If  $R_1 = R_2 = R_3 = R_4 = R_5 = R_6 = 33 \, k\Omega$ , determine the gain resistance  $R_G$  that gives  $V_O = 7V$ .



(d) The 7V analogue output from the instrumentation amplifier in (b) is to be converted to a digital word. A 3-bit Flash ADC is to be used.

Describe using a circuit diagram how the digital conversion occurs.

### Question 4 (20mks)

(a) The data in the table gives the strain values against differential [4mks] pressure for a strain gauge bonded to bellows. Pressure ranges from 120kPa to 210kPa.

Pressure (kPa)	120	150.3	177.5	210
Strain (µm)	70	85	100	115

Assuming a linear relationship between the minimum and maximum values, determine the non-linearity error as a percentage of full range input at  $\varepsilon = 100 \mu m$ .

- (b) With aid of a well labelled diagram, describe the structure of a force [6mks] sensitive resistor, explain the working principle and give TWO applications.
- (c) Using well labelled circuit diagrams, explain how voltage-to- [5mks] frequency converters generate a frequency signal.
- (d) Describe the working of a variable reluctance tachogenerator. [5mks]

  Question 5 (20mks)
- (a) With the aid of a well labelled circuit diagram, describe how an LVDT [5mks] transducer can be used with a bellows to measure changes in fluid pressure levels.
- (b) An LVDT with a range of ±10mm is connected to a 15V voltmeter [5mks] through an amplifier of gain 250. The voltmeter scale has 100 divisions and can be read up to 1/5th of a division. A 0.4mm core displacement produces a differential output of 2mV Calculate:
  - i) The sensitivity of the LVDT at the amplified output
  - ii) The output resolution of the instrument
  - iii) Number of bits required for an ADC when its output is to be digitized
- (b) Four Piezoelectric crystals of charge sensitivity 2Pc/N, area =  $1\text{cm}^2$ , [10mks] 0.1 cm thickness and  $\varepsilon_r = 5$ . They are arranged in parallel, and are subjected to a force of 24N.

Take  $\varepsilon_0 = 8.85 \times 10^{-12} \text{F/m}$ ;  $E = 9 \times 10^{10} \text{Pa}$  Determine:

- i) the voltage across the electrodes for each crystal
- ii) the change in crystal thickness
- iii) If the output voltage from the sensor is fed through a charge amplifier, into a 16-bit ramp A/D converter with 1.5V resolution. Assuming uniform quantization error of ±0.05125V, calculate the range of possible input signals.